

Product Change Notification

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Printed form FC32S00970 / Rev M.1

Product Change Notification Number : GC184059

Date: 09 October 2018

Title: PC7448 Maximum Junction Temperature modification

Product Identification:

PC7448FGH1267ND
 PC7448MGH1267ND

PC7448FLH1267ND
 PC7448MLH1267ND

PC7448FSH1267ND
 PC7448MSH1267ND

Reason for Change:

Other

Design

Manufacturing Location

Processing

Quality/Reliability

Logistics

Material

Change Description:

To avoid functionality limitations, the maximum junction temperature (Tj) of the PC7448 must be reduced for certain temperature range conditions.

- The maximum junction temperature of the products PC7448E_{xx}1267ND is restricted to T_j=105°C instead of T_j=+125°C previously specified.
- For the product PC7448E_{xx}1267ND with the derated conditions: F core = 1000Mhz at V_{dd} = 1.0V +/- 50mV., the maximum junction temperature still remains at T_j=+125°C.
- The PC7448M_{xx}1267ND: T_c=-55°C, T_j=+125°C" temperature range is no longer available for 1267MHz processor frequency and removed from the new datasheet rev J.

Here below, an extract of the new part numbering description of the PC7448 datasheet reference 0814 rev J:

xx	7448	y	xx	nnnn	N	x
Product code	Part Identifier	Temperature Range	Package	Processor Frequency	Application Modifier	Revision Level
PC(X)	7448	F: T _c =-40°C, T _j =+105°C	GH: Hi-TCE CBGA LH: Hi-TCE LGA SH: ROHS BGA	1267 MHz (5)	N: 1.05V +/- 50mV	D:2.2 PVR=8004_0202
Note	5	Part : PC7448F_{xx}1267ND For the derated conditions, Processor Frequency = 1000MHz at V_{dd} = 1.00V +/- 50mV, the functionality is guaranteed at T_j=125°C maximum				

Identification Method to Distinguish Change:

All devices marked PC7448FGH1267ND, PC7448FLH1267ND or PC7448FSH1267ND, with Assembly DC 1724 and afterwards are affected by this specification change.

Qualification Data:

available

will be available in WW__

not applicable

Samples:

available

will be available in WW__

not applicable

Quantifiable Impact on Quality & Reliability:

No impact

Implementation Date*:

November 1st 2018

*The Estimated Implementation Date is the forecasted date that a customer may expect to receive changed product. This is determined by the estimated date of inventory depletion on the PCN issue date. This may be affected by fluctuations in supply and demand. Consequently, although customers should be prepared to receive changed product on this date, Teledyne e2v semiconductors SAS will continue to ship pre-changed product until a time in which inventory has been depleted. This may result in pre-changed product being shipped to customers after this forecasted date.

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Teledyne e2v semiconductors SAS contact:

	Quality	Marketing
NAME	JP.Tournier	L.Giry
EMAIL:	HOTLINE-STD@teledyne-e2v.com	semiconductors.MKT@teledyne-e2v.com

APPROVAL by TRB

Teledyne e2v semiconductors SAS will deem this change accepted unless specific conditions of acceptance are provided in writing within 30 days from the date of this notice. All correspondence must be sent to the contact e-mail addresses indicated above.

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